

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: **60069771 A**(43) Date of publication of application: **20.04.85**

(51) Int. Cl.

G06F 15/21
G07G 1/12
(21) Application number: **58177225**(71) Applicant: **FUJITSU LTD**(22) Date of filing: **26.09.83**(72) Inventor: **KOBAYASHI TAKAFUMI**(54) **CONTROL CIRCUIT OF PROCESSING REQUEST INFORMATION**

COPYRIGHT: (C)1985, JPO&Japio

(57) Abstract

PURPOSE: To obtain a control circuit which processes plural kinds of processing request information from a circuit interface part and a circuit data processing part in a queue buffer format and also processes processing requests in time series according to priority.

CONSTITUTION: Significant information received by a digital subscriber circuit interface part 3 is sent to the circuit data processing part 2 through a data bus 6 and stored in a data queue 9 in it or stored in the queue 9 after being processed upon occasion. The circuit data processing part 2 informs processing request information queue 4 of a processing request through a processing request data line 8. An interruption line 7 operates for the queue 4 successively from the processing request with top priority to send an interruption request to a main processing part 1. When requests have the same priority, the interruption line 7 operates in time series successively in arrival order to send the interruption request to the main processing part 11.

